



Dnyaan Prasad Global University

Dr. D. Y. Patil Unitech Society

(Recognised by UGC & Established under Govt. of Maharashtra Act No. 20 of 2024)

Ref. No.: DPGU/EXAM/2026/069

Date: 07/05/2026

Circular No. 25

University Term End Examination Timetable for Spring Semester (A.Y. 2025-26)

School of Technology and Research

B.Tech. Computer Science and Engineering (2025-2026 Batch)

SEMESTER- I (Backlog)

Date	Day	Course Code	Course Name	Time
21.05.2026	Thursday	BTCSEESC107P	Problem Solving through programming Lab.	10:00 am to 01:00 pm

SEMESTER- II (Regular)

Date	Day	Course Code	Course Name	Time
18.05.2026 to 20.05.2026	Monday to Wednesday	BTCSEPCC160P	Computational Thinking & Programming Lab	09:00 am to 05:00 pm

B. Tech. (Computer Science and Engineering)- Lateral / Second Year

SEMESTER- IV (Regular)

Date	Day	Course Code	Course Name	Time
18.05.2026	Monday	BTCSEVSEC261P	WebX Design and Development Lab	09:00 am to 05:00 pm
19.05.2026	Tuesday	BTCSEPCC262P	Computer Networks and Security Lab	09:00 am to 05:00 pm

M. Tech. CSE

SEMESTER- II (Regular)

Date	Day	Course Code	Course Name	Time
01.06.2026	Monday	MTCSEPCC553P	Advanced Computer Networks & Security Lab.	10:00 am to 01:00 pm

M. Tech. CSE (FWP)

SEMESTER- II (Regular)

Date	Day	Course Code	Course Name	Time
01.06.2026	Monday	MTCSWPCC553P	Advanced Computer Networks & Security Lab.	10:00 am to 01:00 pm




Subhash Nalawade
Controller of Examinations(I/c)